Environmental Impacts of Crystalline Silicon Photovoltaic Module Production

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Abstract
Together with a number of PV companies an extensive effort has been made to collect Life Cycle Inventory data that represents the current status of production technology for crystalline silicon modules. The new data cover all processes from silicon feedstock production to cell and module manufacturing. All commercial wafer technologies are covered, that is multi- and monocrystalline wafers as well as ribbon technology. The presented data should be representative for the technology status in 2004, although for monocrystalline Si crystallisation further improvement of the data quality is recommended. On the basis of the new data a Life Cycle Assessment has been performed, which shows that c-Si PV systems are in a good position to compete with other energy technologies. Energy Pay-Back Times of 1.7-2.7 yr are found for South-European locations, while life-cycle CO₂ emissions are in the 30-45 g/kWh range. Clear perspectives exist for further improvements of roughly 40-50%.

Keywords
Solar cells, Photovoltaic systems, Life Cycle Assessment, Energy Pay-Back Time

1 INTRODUCTION
Reliable data on the environmental impacts of Photovoltaic (=PV) module manufacturing have been rather scarce for the last 10-15 years. The only extensive data collection based on production data were published in 1992 [1] and were based on technology from the late 80’s. Later work was done to update these data but this was to a large extent based on secondary data sources and estimates [2, 3, 4]. Consequently, life cycle assessment and external cost studies were often based on the old data set that do not really reflect the technological progress made over the past decade.

In a unique collaboration with 11 PV companies from Europe and the USA, we have improved this situation. The contributing companies together cover the complete production chain for crystalline silicon PV modules, from silicon feedstock production to module assembly. Also they cover all three major technologies for crystalline silicon wafers, namely multicrystalline, monocrystalline and ribbon silicon wafer technology. Ribbon silicon is a technology for producing wafers directly from molten silicon without sawing, thus avoiding significant material losses (see figure 1).

In this paper we present the results of a Life Cycle Assessment study on the basis of these new data.

2 DATA COLLECTION
Together with 11 photovoltaic companies an extensive effort has been made to collect LCI data that represents the status of production technology for crystalline silicon modules, a module type which was used in 94% of all 1200 MW of produced solar cell modules in 2004. All data were collected in the period September 2004 – November 2005. Total solar cell production for the included facilities totalled about 160 MWp, all of them located in Europe.

Also for multi-Si wafer production we cover a sizable share of the European market, while for ribbon technology we probably cover all production capacity in the world. For mono-Si crystallization data quality and market coverage is less good, it is mainly based on literature data from facilities in the USA and Western-Europe. All-in-all we consider our data set as a major improvement over previous work because: 1) it is based to a large extent on measured data from several sources and 2) it represents the actual technology status for 2004. The entire LCI data set is publicly available [5].

3 TECHNOLOGY ASSUMPTIONS, LCA METHOD AND BACKGROUND DATA
With regard to wafer dimensions we assumed 125x125 mm as the standard size for all wafer technologies (including ribbon). Where actual cell or wafer dimensions differed from this standard we scaled energy and material consumption correspondingly. Although 150x150 mm wafer technology is growing fast in market share, this up-scaling in wafer area will have only minor effects on LCA results in terms of impacts per m² module or per kWp power. Wafer thickness was in the range of 270-300 µm for mono- and multi-Si wafers and 300-330 µm for ribbon wafers.

We considered only one standard module type with 72 cells (1.25 m² module area), with glass/EVA/Tedlar lamination. Glass thickness was set at 3.6 mm. We looked at both laminates (i.e., unframed modules) and framed modules, which have an aluminium frame of 3.8 kg. Module efficiencies were roughly based on commercially available modules of each specific technology (Table 1). Module life time was assumed to be 30 years; the life time of electronic components of the PV systems (inverter) was set at 15 years.

<table>
<thead>
<tr>
<th>Ribbon Si</th>
<th>Multi-Si</th>
<th>Mono-Si</th>
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<tr>
<td>11.5%</td>
<td>13.2%</td>
<td>14%</td>
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Table 1: Assumed module efficiencies per technology (measured on full-area, without frame).

Because we focus mainly on module production our functional unit is in principle 1 kWp of modules. Since most energy and material inputs are directly related to the cell or module area, while the specific power rating of modules (in Wp/m²) may vary between manufacturers, we

¹ The kiloWatt-peak (kWp) is a unit for PV module power output under a standard irradiation of 1 kW/m².
will often show results per m$^2$ module area. Furthermore, comparisons with other energy technologies will be done with 1 kWh of generated electricity as the functional unit.

The LCA analyses were performed with the Simapro software (version 6.04) and the Ecoinvent 2000 database. Impact assessments were made on the basis of the CML 2000 baseline method, as implemented in Simapro, with normalisation factors for West Europe 1995. Greenhouse gas emissions are evaluated with IPCC 2001 data for a timeframe of 100 years, while the Cumulative Energy Demand (CED) is calculated by the method described in Ecoinvent 1.01, where we have summed all fossil, nuclear, hydro and renewable energy demand into one single CED value.

Most of the background data for our LCA, such as inventories for production of glass, chemicals and metals were based on the Ecoinvent database version 1.2 [6], with own additions for PV-specific materials. The production of metallurgical-grade silicon was also modelled on the basis of Ecoinvent data. The scope of our analysis comprises all production processes for manufacturing of crystalline silicon solar cell modules, from silica mining up to module assembly (figure 1). End-of-life disposal of modules is not included here, but elsewhere an LCA study of a pilot-scale process for module recycling has been presented [7]. Environmental impacts during the use phase of a PV system are generally negligible, the replacement of electronic components (inverter) after 15 years is accounted for by including two inverters in the system.

For all manufacturing processes, except the production of purified polycrystalline silicon (“poly-Si”), we assumed the average electricity supply system for the Western-European continent (UCTE region), at medium voltage level, as given by the Ecoinvent database. This system has an overall conversion efficiency of 31% and a greenhouse gas emission of 0.48 kg CO$_2$-eq/kWh. For the poly-silicon production the electricity supply was specifically adapted to the two considered facilities, and based on respectively 100% hydropower and a mixture of hydropower and combined cycle gas turbine generation.

4 LCA RESULTS FOR PV MODULES

First we will give LCA results for modules, identify the most significant impacts and analyse the contributions from the specific process steps and input materials.

Figure 2 gives the normalised results for the three module types (functional unit 1 kWp). We see that in all impact categories the monocrystalline technology has the highest impacts score, while ribbon modules have the lowest impacts. We will see later that this is mainly related to the energy demand in the production processes. When comparing impact categories we see that abiotic resource depletion, global warming and acidification are the most significant impacts, while ozon layer depletion gives a very small normalised impact score. It can be shown that the first three impacts are all dominated by emissions from energy conversion processes: the abiotic depletion because this includes the depletion of fossil fuels, the global warming and acidification because of the emissions from fuel combustion. This strong link with energy demand means that if we are able to reduce energy consumption we will also reduce the main environmental impact scores.

Because energy input is an important factor we will now analyse this aspect in more detail. Figure 3 gives the Cumulative Energy Demand (CED) for the three module types, expressed in MJ of primary energy (MJ$_p$) per m$^2$ of module area$^3$.

Some impact categories of the original CML method (all toxicity impacts) were omitted because there are still too much scientific uncertainties with regard to the impact evaluation in these categories.

$^3$ Results are here depicted per unit area because all major (energy) inputs are area-related.
Figure 2: Normalised LCA results for the three module types (f.u. = 1 kWp; normalization: W.-Europe 1995).

Figure 3: Energy input for crystalline silicon modules, in MJ of primary energy per m² of module area, with the contributions of consecutive process steps.

Figure 4: Energy input breakdown by source for a multi-Si wafer (125x125 cm², 285 um thick)
The relative magnitude of the cumulative energy demand for the three module types is clearly similar to the relative impact scores for abiotic depletion, global warming and acidification in figure 2, a similarity which supports our assertion that these impacts are dominated by energy conversion processes.

Contributions from the different process steps are also shown in figure 3 and we can see that poly-silicon production has a large contribution, but also crystallization and wafering, especially for mono-Si material. As mentioned above, however, data for this latter process step is less good as we would like. The energy input for ribbon modules is the lowest, which is mainly due to reduced poly-Si consumption (no silicon loss from wafer sawing) and also because of lower energy requirements in the crystallization & wafering step. The processes of cell and module production, which are the same for all three technologies, have less important contributions to the cumulative energy demand. It is noted that the figure for monocrystalline silicon comes out relatively high because of the assumed higher energy consumption of Cz crystal growth. The situation will become more favorable if more (new) facilities with energy efficient hot zones in the Cz grower and recharging of the silicon are taken into operation.

Figures 4 and 5 give a different break-down of the energy input for respectively a multi-Si wafer and a module of the same type. Most notable in figure 4 is that for wafer production not only the poly-Si material has an important contribution, but also the silicon carbide and other materials that are used in wafer cutting. This share will grow when wafer thickness is further reduced. In Figure 5 we can see that apart from the wafer itself the encapsulation materials (glass, EVA, back foil) and the frame also have a significant share in the energy requirement of a module.

5 EVALUATION OF PV SYSTEMS

We will now look at complete PV systems and evaluate these in terms of their Energy Pay-Back Time (EPBT) and life cycle CO₂ emissions.

For the evaluation of the Energy Pay-Back Time of a PV system we assume that the modules are installed in a grid-connected roof-top system with a Performance Ratio of 0.75⁴. Under a 1700 kWh/m²/yr irradiation (Southern-Europe) the system can thus generate 1275 kWh/kWp/yr of electricity. If this electricity is fed back to the same electricity supply system that was used for manufacturing (this is not necessarily the case), then we can save 14780 MJ of primary energy per kWp per year. Further assumptions for the energy input of the BOS components are taken from a previous study [3] and summarized in table 2.

<table>
<thead>
<tr>
<th>Component</th>
<th>Energy Input</th>
<th>CO₂-equivalent Emissions</th>
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<tbody>
<tr>
<td>Array support + cabling</td>
<td>100 MJ/m²</td>
<td>6.1 kg/m²²</td>
</tr>
<tr>
<td>Inverter</td>
<td>1930 MJ/kWp</td>
<td>125 kg/kWp</td>
</tr>
</tbody>
</table>

Table 2: Energy and CO₂ data of BOS components used for EPBT and CO₂ emission evaluations on a system level [3]. Inverter data includes one replacement half-way the system life.

Figure 6 shows the resulting Energy Pay-Back Times in years. We can see that EPBT's are in the range of 1.7-2.7 years for a South-European location, while for Middle-Europe (irradiation 1000 kWh/m²/yr) locations we obtain higher EPBT values in the 2.8-4.6 year range.

From figure 6 it is clear that the laminate (unframed module) dominates the energy pay back results, while the Balance-of-System has a relatively small contribution. The contribution of the module frame is shown separately because its contribution relatively large and sometimes systems are built with frameless modules (laminates) too.

Also remark that the difference between ribbon, multi- and mono-Si has decreased in comparison with figure 3. This is of course due to the differences in module efficiency, with ribbon having somewhat lower and mono-Si a bit higher value (cf. Table 1).

Based on our LCA results regarding greenhouse gas (GHG) emissions for module manufacturing we can in a similar way as above evaluate the life-cycle GHG emissions of our PV system, expressed in kg CO₂-equivalent per kWh.

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⁴ This means that a 1 kWp system under an irradiation H (in kWh/m²/yr) will generate Eₚ=PR·H of electricity (in kWh/yr). Note that this PR value might be somewhat conservative, especially for systems in Southern-Europe.
Figure 6: Energy Pay-Back Time (in yr) for a grid-connected PV-system under an irradiation of 1700 kWh/m²/yr (Southern-Europe) respectively 1000 kWh/m²/yr (Middle-Europe).

Figure 7: Greenhouse gas emissions of PV systems based on three silicon technologies, compared to a number of other energy technologies. The PV systems are installed on a roof-top in S.-Europe (irradiation 1700 kWh/m²/yr) and have a 30 year life time. N.B. The emission from a coal-fired power plant (1000 g/kWh) exceeds the Y-axis maximum ! (Sources: Coal, CC gas, nuclear, biomass and wind data derived from Ecoinvent database [6])

Figure 8: Energy Pay-Back Time for future multi-Si and ribbon technology (two bars at the right), compared with today’s status (South Europe). The numbers below the X-axis give the respective module efficiencies.
For this evaluation we further use the BOS data in table 2 and assume a 30 year system life time. Figure 7 shows the results of this exercise, and compares today’s PV systems with a number of other energy supply options. We can see that PV with a GHG emission of 30-45 g CO₂-eq, performs quite well in comparison with fossil-fuel-based technologies, but less so in comparison with wind and nuclear technology.

6. FUTURE DEVELOPMENTS

It is also interesting to look what developments may be possible in the future. In poly-silicon feedstock production the application of Fluidized Bed Reactors (FBR) for silicon deposition could significantly reduce electricity consumption. Also we have clear indications that construction of new facilities for multi-Si casting and wafering could reduce energy consumption for this process significantly too. Within the Crystal Clear project it is furthermore an aim to reduce wafer thickness to 150 μm. For cell and module manufacturing reduction options are less clear however, and energy input may even increase for example by introduction of clean room environments. We do assume however that module efficiency can be increased significantly without increasing process energy (or material) consumption. Finally we assume that frameless modules become the standard technology. Table 3 gives an overview of the improvements that we consider feasible for ribbon and multi-Si technology within the next 5-8 years. We refrained from an analysis of mono-Si technology because our data basis for current mono-Si technology still has too much uncertainty. When we analyse the production of such modules we find that significant reductions in EPBT are possible, to about 1 year for a South-European location (Figure 8). The introduction of FBR technology for silicon feedstock production plays an important role in this improvement because it can reduce the electricity consumption for poly-silicon deposition by at least 70%. Under the same assumptions life cycle CO₂ emissions could drop to 17 and 20 g/kWh respectively for ribbon and multi-c-Si technologies, bringing them nicely in the same range as other renewable energy technologies. There are also definitive points of attention for the future. If the use of fluorinated gases for dry etching increases it is very important that proper emission abatement equipment is installed. At this moment CF₃ is already used in some production facilities and not always abatement equipment is in place. For a CF₃ consumption of 40 kg per MWp which is emitted unabated, the total greenhouse gas emission of modules may increase by 20%!

6 CONCLUSIONS

Together with a number of PV companies an extensive effort has been made to collect Life Cycle Inventory data that represents the current status of production technology for crystalline silicon modules. On the basis of these new data it is shown that PV systems are in a good position to compete with other energy technologies. Energy Pay-Back Times of 1.7-2.7 yr are found for South-European locations, while life-cycle CO₂ emission is in the 30-45 g/kWh range. Clear perspectives exist for further improving these indicators in the near-term by about 40-50%.

Further improvements in data quality are needed in the field of mono-crystalline silicon ingot growing.

ACKNOWLEDGEMENTS

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REFERENCES


Table 3: Assumptions for future multi-Si and ribbon technology

<table>
<thead>
<tr>
<th></th>
<th>Multi-Si</th>
<th>Ribbon-Si</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Si Feedstock</strong></td>
<td>Fluidized Bed Reactor deposition of “Solar Grade” Silicon</td>
<td></td>
</tr>
<tr>
<td><strong>Crystallisation</strong></td>
<td>Best Available Technology 2004</td>
<td>Standard Technology 2004</td>
</tr>
<tr>
<td><strong>Wafer thickness</strong></td>
<td>285 -&gt; 150 um</td>
<td>300 -&gt; 200 um</td>
</tr>
<tr>
<td><strong>Module efficiency</strong></td>
<td>13.2 -&gt; 16%</td>
<td>11.5 -&gt; 15 %</td>
</tr>
<tr>
<td><strong>Module assembly</strong></td>
<td>Frameless module</td>
<td>Frameless module</td>
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